



Docket No.: 1190-0519P
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Yoshito SUZUKI et al.

Application No.: 09/961,245

Confirmation No.: 5739

Filed: September 25, 2001

Art Unit: 2637

For: SYNCHRONIZING SIGNAL
PROCESSING CIRCUIT

Examiner: G. Munoz

LETTER SUBMITTING CORRECTED DRAWINGS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to the USPTO Notice of Allowability Form PTO-37 dated April 21, 2005 attached to the Notice of Allowance and Issue Fee Due Form PTOL-85, attached hereto are 2 (two) sheets of corrected drawings.

Please substitute these corrected drawings for the corresponding 2 (two) sheets of drawings on file in the above-identified application.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fee required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

Dated: July 20, 2005

Respectfully submitted,

By _____
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Attachments